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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/648,189	08/27/2003	Chih-Wei Chen	CHEN3582/EM	3482

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EXAMINER

KIM, HAROLD J

ART UNIT	PAPER NUMBER
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2182

DATE MAILED: 03/22/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/648,189

Applicant(s)

CHIH-WEI CHEN

Examiner

Harold Kim

Art Unit

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 27 August 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-5 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-5 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 27 August 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

1. Claims 1-5 are presented for examination.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

3. **Claims 1-5 are rejected under 35 U.S.C. 102(e) as being anticipated by Chou et al., US Patent no. 6,622,192.**

4. In re claim 1, Chou et al. shows a method of safely shutting down [title] an embedded system based computer under an OS (operating system) [col 3, line 25], the computer including a power switch [10, fig 1] controlled by an input/output (I/O) control chip [col 4, line 48-49], the power switch having a power button [col 5, lines 34-35], the power button being adapted to press for causing the I/O control chip to trigger a system chipset [col 5, line 25] so that a CPU (central processing unit) [col 4, line 46] performs the steps of:

(a) causing the triggered system chip to issue an interrupt request (IRQ) to the CPU and receiving the same responsive to triggering the system chipset by the I/O control chip [col 4, lines 42-49; 21 in fig 1];

(b) activating a service routine of the OS for determining whether the IRQ is issued by the pressed power button [col 5, lines 1-11; 22 and 23 in fig 1]; and

(c) terminating all running processes of the OS and performing a soft-off for stopping supplying power from a power supply to the computer if the determination in the step (b) is positive [24, fig 1].

5. In re claim 2, Chou et al. shows (d) of waiting the system chip to issue an IRQ if the determination in the step (b) is negative [25, fig 1; col 5, lines 38-40].

6. In re claim 3, Chou et al. shows (e) determining whether the power button is pressed or not [23, fig 1];

(f) terminating all processes being run by the OS if the determination in the step (e) is positive [24, fig 1];

(g) writing a set value into a soft-off register of the I/O control chip [col 4, lines 55-67]; and

(h) causing the power switch to stop supplying power from the power supply to the computer responsive to writing the set value into the soft-off register and disabling the same [col 4, line 55 to col 5, line 11].

7. In re claim 4, Chou et al. shows (i) continuously determining whether the power button is pressed or not if the determination in step (e) is negative [col 5, lines 5-6].

8. In re claim 5, Chou et al. inherently shows the system chipset [col 5, line 25] is a south bridge.

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Further references of interest are cited on Form PLO-892, which is attachment to this office action.

Any response to this action should be mailed to:

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The centralized fax number is 703 872-9306.

The centralized hand carry paper drop off location is:

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Crystal Plaza Two, Lobby, Room 1B03

Any inquiry of a general nature or relating to the status of this application should be directed to the central telephone number (571) 272-2100.

Direct any inquiries concerning drawing review to the Drawing Review Branch (703) 305-8404.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Harold Kim whose telephone number is 571-272-4148. The examiner can normally be reached on Monday-Thursday 6AM-4PM.


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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jeffrey Gaffin can be reached on 571-272-4146. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Harold J. Kim
Patent Examiner
March 20, 2005/HK



JEFFREY GAFFIN
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2100